

APR 29 2004

Sheet 1 of 2

FORM PTO-1449		DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 58269.00014	SERIAL NO. 09/943,238
LIST OF REFERENCES CITED BY APPLICANT <i>(Use several sheets if necessary)</i>		APPLICANT TZENG		
		FILING DATE August 31, 2001	GROUP 2661	

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA						
	AB						
	AC						
	AD						APR 30 2004
	AE						Technology Center 2600
	AF						

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FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION YES NO PART.
PB	AG	WO 00/03256	Jan. 20, 2000	WIPO			XX
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	AI						
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PB	AL	Level One: "Level One™ IXP1200 Network Processor", September 1999, XP002187948, URL: http://www.cs.utah.edu/fcs6935/Previous/s01/netprocs/ixa/ixp1200ad.pdf , retrieved on Jan. 22, 2002.
PB	AM	Noureddine W et al: "Selective Back-Pressure in Switched Ethernet LANS" 1999 IEEE Global Telecommunications Conference, Globecom'99, Seamless Interconnection for Universal Services, Brazil, IEEE Global Telecommunications Conference, New York, vol. 2, Dec. 5, 1999, pages 1256-1263, XP002258500.
PB	AN	Doganata Y N et al: "Effects of Cascading on the Performance of a Switching Subsystem", Communication for Global Users, Including a Communications Theory Mini Conference , Orlando, 1992, Proceedings of The Global Telecommunications Conference (Globecom), New York, IEEE, vol. 1, Dec. 6 1992, pages 1493-1497.

EXAMINER

DATE CONSIDERED

07/15/05

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



Sheet 2 of 2

FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 58269.00014	SERIAL NO. 09/943,238
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PB	AL	Mannion P: "10-GBit Ethernet Switching Processor Blasts Barriers to the Backbone", Electronic Design, Penton Publishing, Cleveland, vol. 48, no. 16, Aug. 7, 2000, pages 72-74, 76, XP001076479.
	AM	
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EXAMINER <i>Puneeth Hanandu</i>	DATE CONSIDERED <i>07/15/05</i>
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FORM PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 023925-00014	SERIAL NO. New Application
				APPLICANT TZENG	
				FILING DATE August 31, 2001	GROUP Not yet assigned
				JCP 09/943238 U.S. PRO 08/31/01	
LIST OF REFERENCES CITED BY APPLICANT <i>(Use several sheets if necessary)</i>					

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PB	AA	5,555,398	Sep. 10, 1996	Raman			Apr. 15, 1994
PB	AB	5,644,784	Jul. 1, 1997	Peek			Mar. 3, 1995
PB	AD	5,828,653	Oct. 27, 1998	Goss			Apr. 26, 1996
PB	AE	5,423,015	Jun. 6, 1995	Chung			May 21, 1991
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PB	AG	6,061,351	May 9, 2000	Erimli et al.			Dec. 18, 1997
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PB	AJ	6,175,902 B1	Jan. 16, 2001	Runaldue et al.			Dec. 18, 1997
PB	AK	5,473,607	Dec. 5, 1995	Hausman et al.			Aug. 9, 1993
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PB	AN	5,892,922	Apr. 6, 1999	Lorenz			Feb. 28, 1997
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PB	AP	6,011,795	Jan. 4, 2000	Varghese et al.			Mar. 20, 1997
PB	AQ	6,185,185 B1	Feb. 6, 2001	Bass et al.			Nov. 21, 1997
PB	AR	5,524,254	Jun. 4, 1996	Morgan et al.			Jul. 1, 1994
PB	AS	5,987,507	Nov. 16, 1999	Creedon et al.			Dec. 29, 1998
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PB	AU	5,781,549	Jul. 14, 1998	Dai			Feb. 23, 1996
PB	AV	6,041,053	Mar. 21, 2000	Douceur et al.			Sep. 18, 1997
PB	AW	5,459,717	Oct. 17, 1995	Mullan et al.			Mar. 25, 1994
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BB	BF	5,898,687	Apr. 27, 1999	Hartman et al.			Jul. 24, 1996
BB	BG	5,940,596	Aug. 17, 1999	Rajan et al.			Aug. 4, 1997
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PB	BM	5,652,579	Jul. 29, 1997	Yamada et al.			Aug. 15, 1996
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BB	BP	EP 0 465 090 A1	Jan. 8, 1992	EPO			xx
BB	BQ	EP 0 859 492 A2	Aug. 19, 1998	EPO			xx
BB	BR	EP 0 312 917 A2	Apr. 26, 1989	EPO			xx
BB	BS	EP 0 853 441 A2	Jul. 15, 1998	EPO			xx
BB	BT	4-189023	Jul. 7, 1992	Japan			Abs
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BB	BY	EP 0 854 606 A2	Jul. 22, 1998	EPO			xx
BB	BZ	WO 99/00948	Jan. 7, 1999	WIPO			xx
BB	CA	WO 99/00949	Jan. 7, 1999	WIPO			xx
BB	CB	WO 99/00950	Jan. 7, 1999	WIPO			xx
BB	CC	WO 99/00939	Jan. 7, 1999	WIPO			xx
BB	CD	WO 99/00938	Jan. 7, 1999	WIPO			xx
BB	CE	WO 99/00944	Jan. 7, 1999	WIPO			xx
BB	CF	WO 99/00945	Jan. 7, 1999	WIPO			xx
BB	CG	EP 0 849 917 A2	Jun. 24, 1998	EPO			xx
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<i>PB</i>	CJ	"Local Area Network Switch Frame Lookup Technique for Increased Speed and Flexibility," 700 IBM Technical Disclosure Bulletin 38(1995) July, No. 7, Armonk, NY, US, pages 221-222
<i>PB</i>	CK	"Queue Management for Shared Buffer and Shared Multi-buffer ATM Switches," Yu-Sheng Lin et al., Department of Electronics Engineering & Institute of Electronics, National Chiao Tung University, Hsinchu, Taiwan, R.O.C., March 24, 1996, pages 688-695
<i>PB</i>	CL	"A 622-Mb/s 8 x 8 ATM Switch Chip Set with Shared Multibuffer Architecture," Harufusa Kondoh et al., 8107 IEEE Journal of Solid-State Circuits 28(1993) July, No. 7, New York, US, pages 808-814
<i>PB</i>	CM	"Catalyst 8500 CSR Architecture," White Paper XP-002151999, Cisco Systems Inc. 1998, pages 1-19
<i>PB</i>	CN	"Computer Networks," A.S. Tanenbaum, PRENTICE-HALL INT., USA, XP-002147300(1998), Sec. 5.2-Sec. 5.3, pages 309-320

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